Yitai Hu 202.408.4203

> FEE VALUE ACCOUNTABILITY

DEPOSIT ACCOUNT NO.

CODE

VALUE

PUTANISHED

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February 5, 2002

ATTORNEY DOCKET NO. 06720.0070 CUSTOMER NUMBER: 22,852년

**Box PATENT APPLICATION Assistant Commissioner for Patents** Washington, D.C. 20231

New U.S. Patent Application

Title: SCR DEVICES IN SILICON-ON-INSULATOR CMOS PROCESS FOR ON-CHIP ESD PROTECTION

Inventor(s): MING-DOU KER ET AL

We enclose the following papers for filing in the United States Patent and Sir: Trademark Office in connection with the above patent application.

- Application 26 pages, including 3 independent claims and 35 claims total. 1.
- Drawings 10 sheets of formal drawings (Figures 1-8B). 2.
- Declaration/Power of Attorney.
- Recordation Form Cover Sheet Assignment to Industrial Technology Research 3. 4. Institute

1

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The filing fee is calculated as follows:					\$740	\$	740.00
Basic Application Filing	Number of Claims		Basic	Extra Claims			
	35	+-	20	15	x \$18	_	270.00
Total Claims		╁	3		x \$84		
Independent Claims	3				+\$270		
[] Presentation of Multiple Dep. Claim(s) Subtotal						\$	1010.00
Reduction by 1/2 if small entity						T	
TOTAL APPLICATION FILING FEE						\$	1010.

A check for \$1050.00 is enclosed. The fee includes: 3.

\$ 740.00 filing fee;

\$ 270.00 additional claim fee;

\$ 40.00 Recordation Fee

Please address all correspondence with respect to this application to:

Finnegan, Henderson, Farabow, Garrett & Dunner, L.L.P. 1300 I Street, N.W. Washington, D.C. 20005-3315

Please accord this application a serial number and filing date.

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The Commissioner is hereby authorized to charge any additional filing fees due and any other fees due under 37 C.F.R. § 1.16 or § 1.17 during the pendency of this application to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER, L.L.P.

Reg. No. 40,653

YH:pwl Enclosures